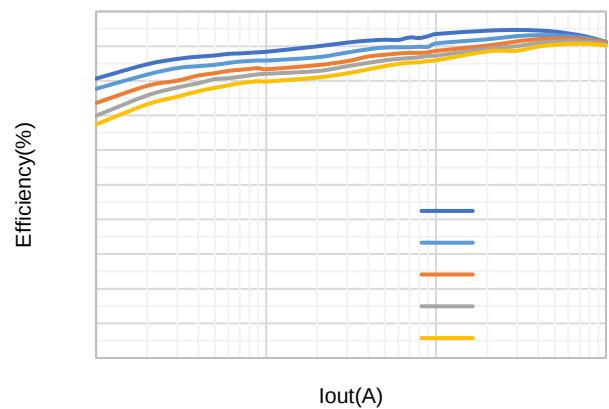
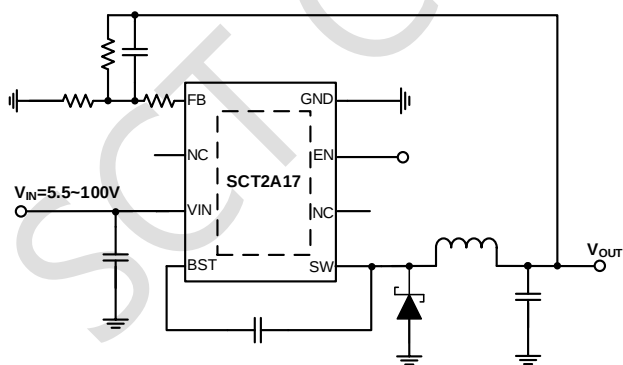


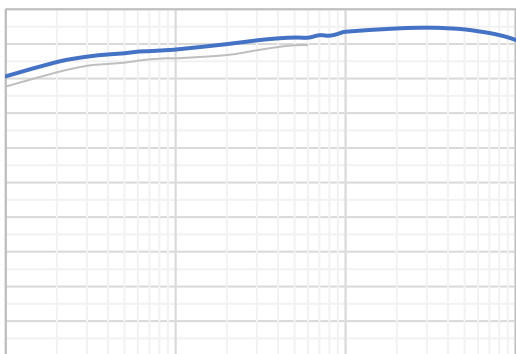
5.5V-100V Vin, 1.8A Peak Current Limit, High Efficiency Asynchronous Step-down DCDC Converter

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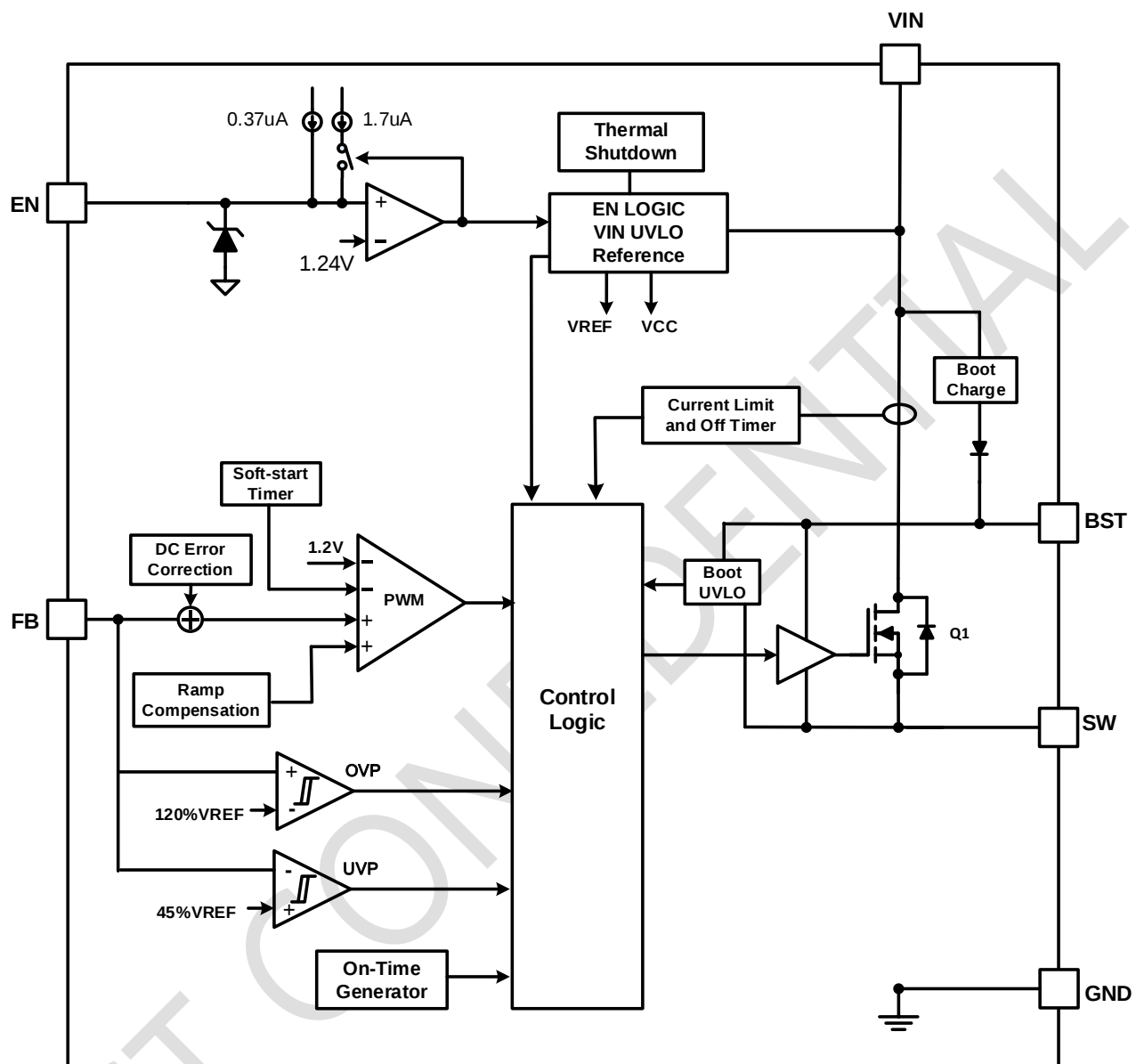


SCT CONFIDENTIAL

(1)
(2)



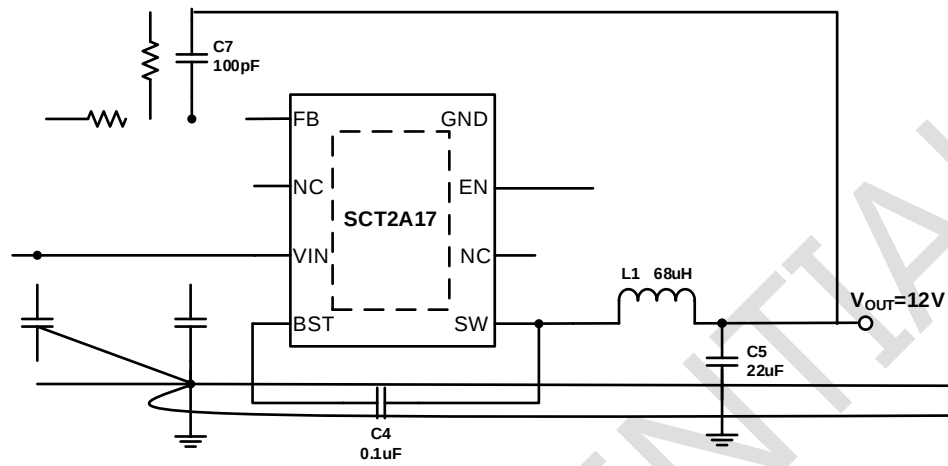
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C4

Table 1. R₁, R₂ Value for Common Output Voltage
(Room Temperature)

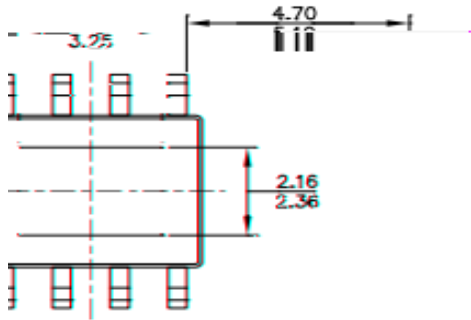
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SCT CONFIDENTIAL

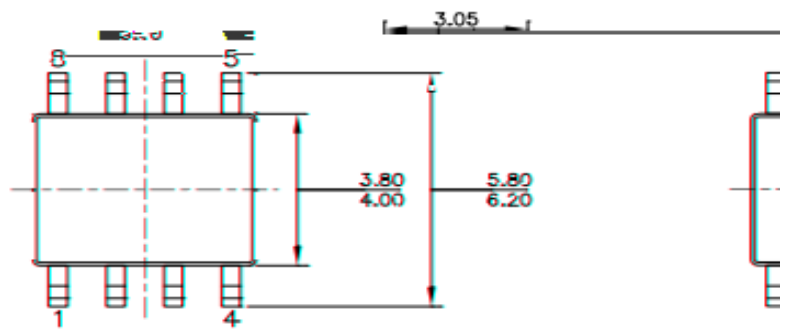
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SCT CONFIDENTIAL

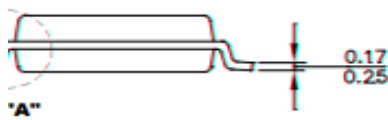
PACKAGE OUTLINE DRAWING FOR 8L SOP-EP POD-0022 Revision 0.0



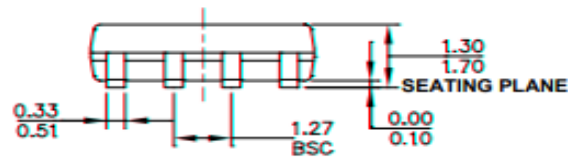
TOP VIEW



TOP VIEW



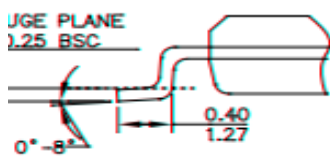
SIDE VIEW



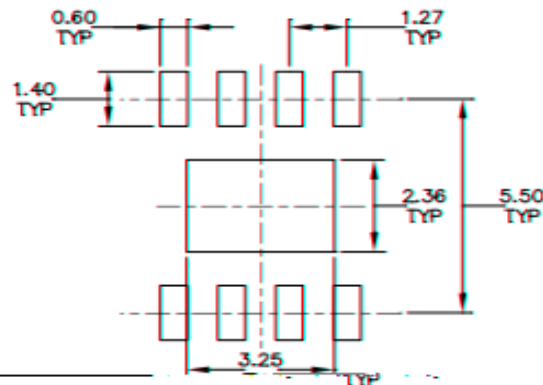
FRONT VIEW



SEE DETAIL "A"



DETAIL "A"



RECOMMENDED LAND PATTERN

NOTE:

1) ALL DIMENSIONS ARE IN MILLIMETERS.
2) DIMENSIONS DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
3) DIMENSIONS SHALL BE 0.004" INCHES MAX.

- 1) CONTROL DIMENSION IS IN INCHES. DIMENSION IN BRACKET IS IN MILLIMETERS.
- 2) PACKAGE LENGTH DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
- 3) PACKAGE WIDTH DOES NOT INCLUDE INTERLEAD FLASH.
- 4) LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.004" INCHES MAX.
- 5) DRAWING REFERENCE TO JEDEC MS-012, VARIATION AA.
- 6) DRAWING IS NOT TO SCALE.

