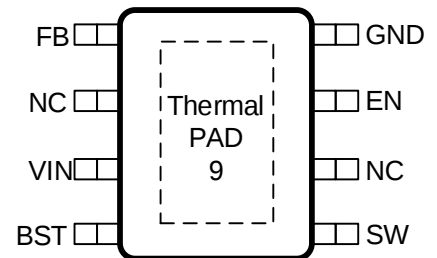
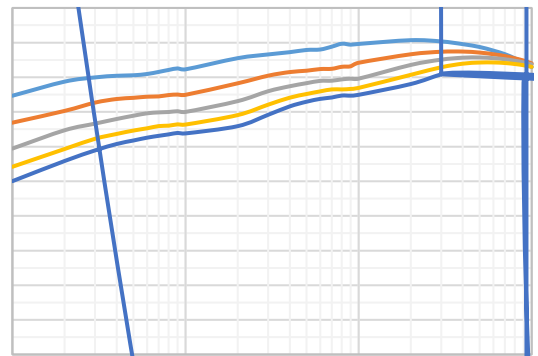
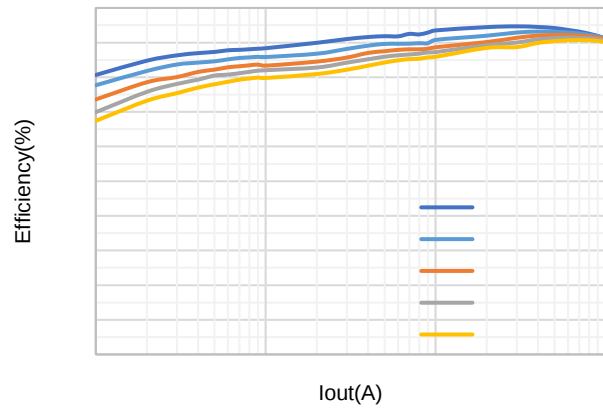
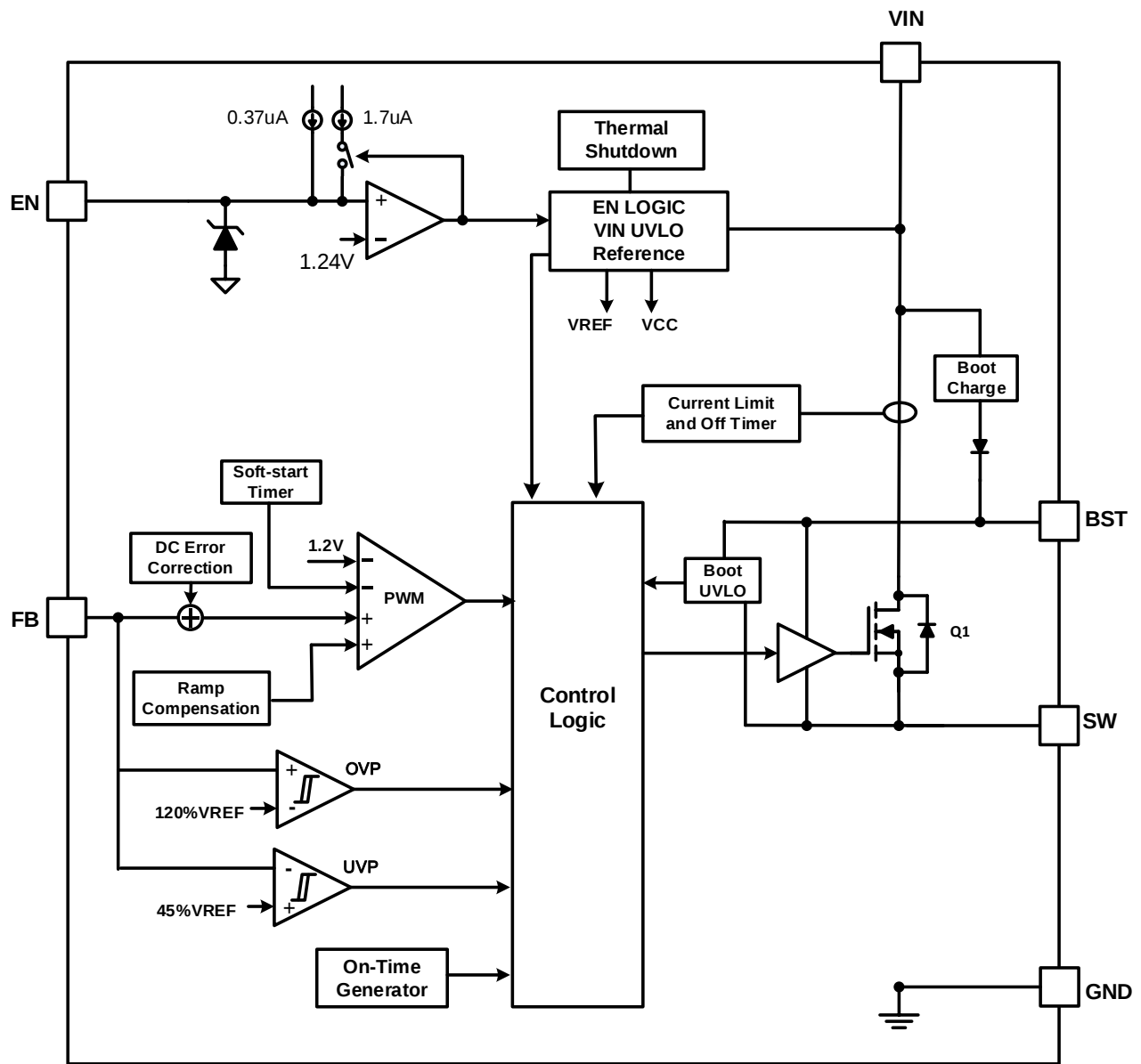


5.5V-100V Vin, 1.8A Peak Current Limit, High Efficiency Asynchronous Step-down DCDC Converter



- (1)
- (2)
- (3)





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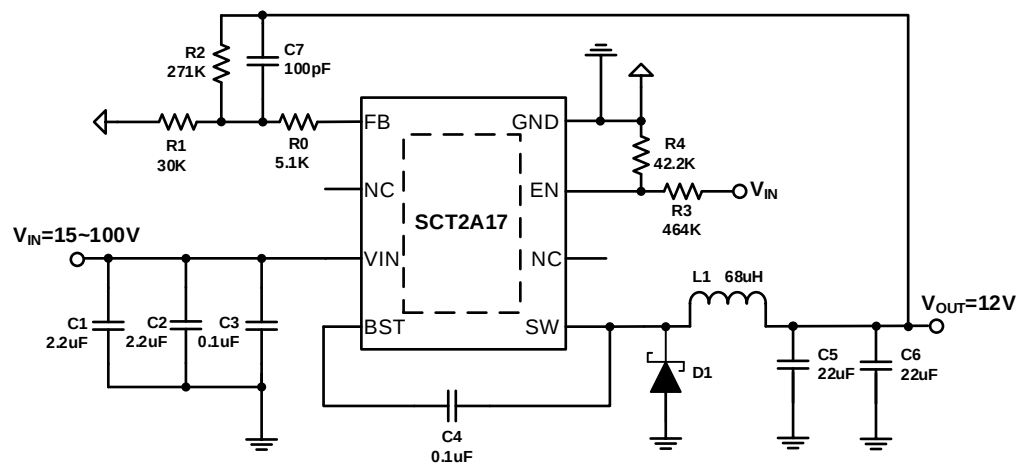


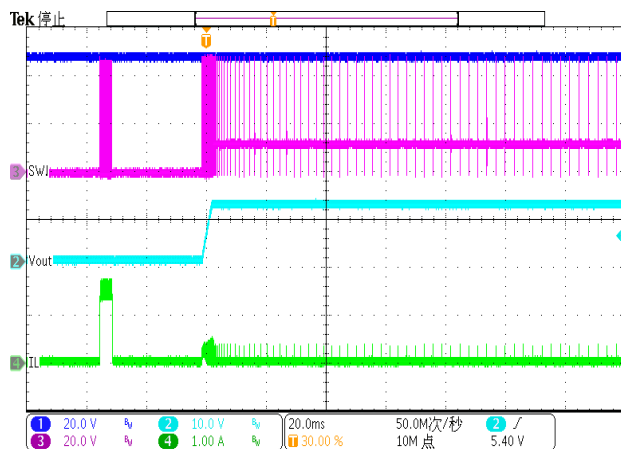
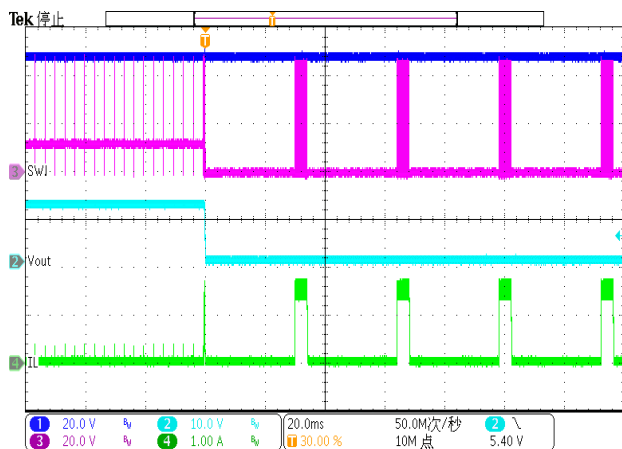
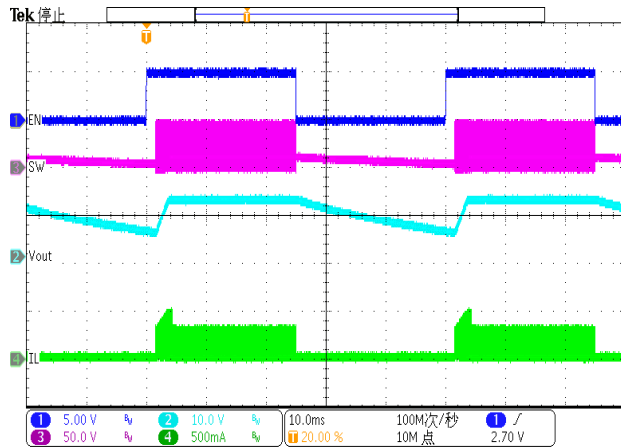
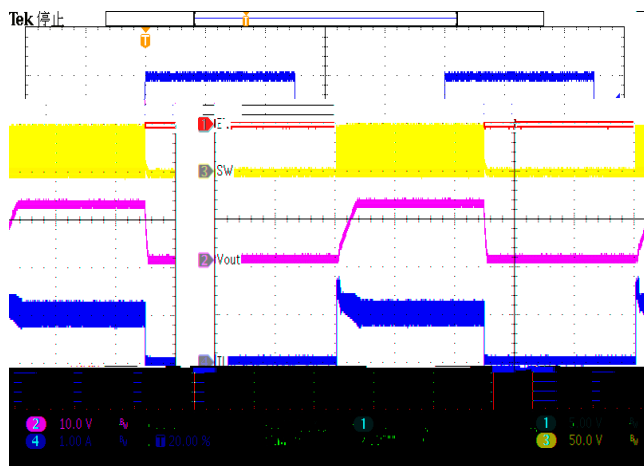
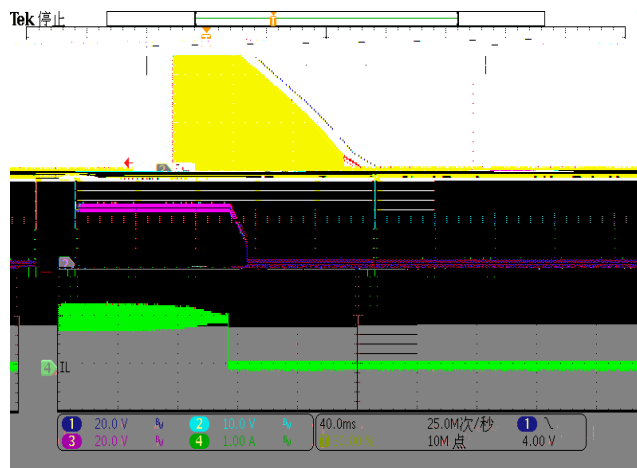
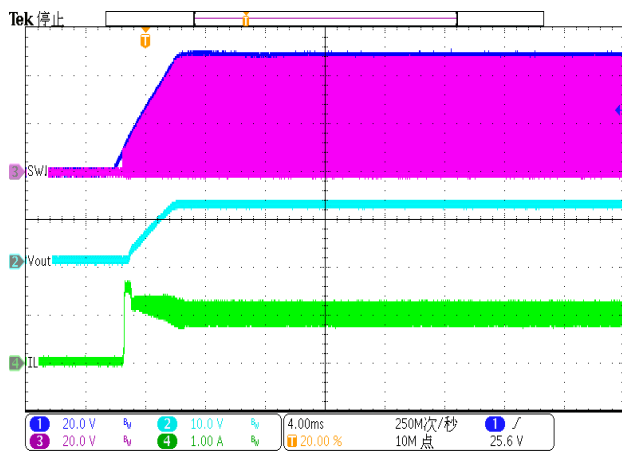
Table 1. R_1 , R_2 Value for Common Output Voltage
(Room Temperature)

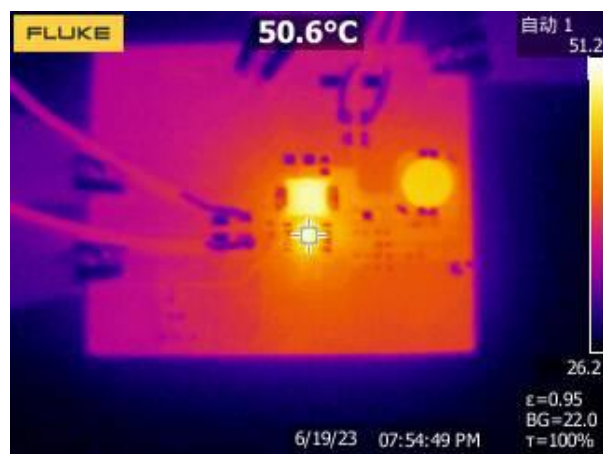
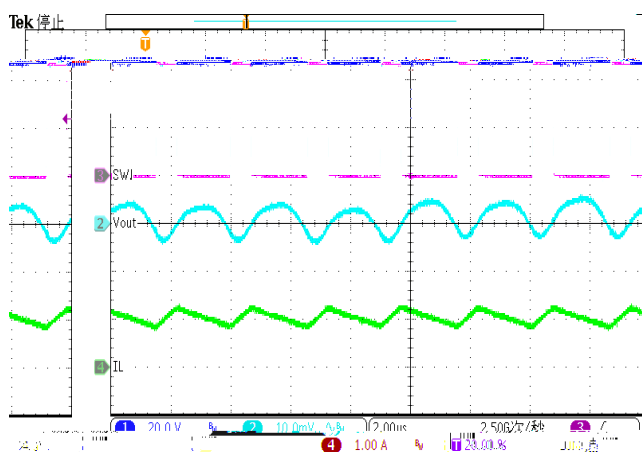
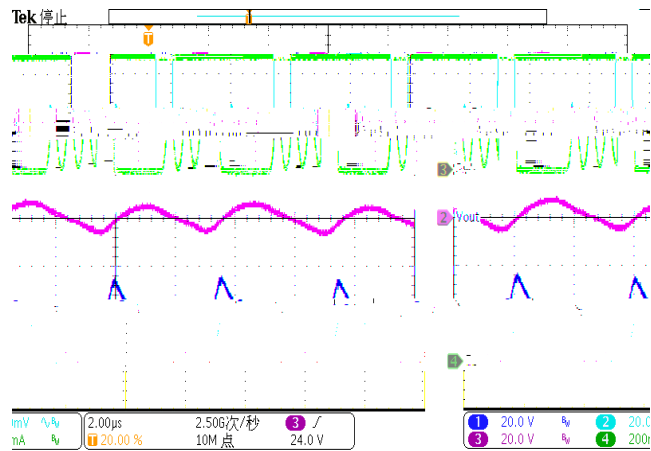
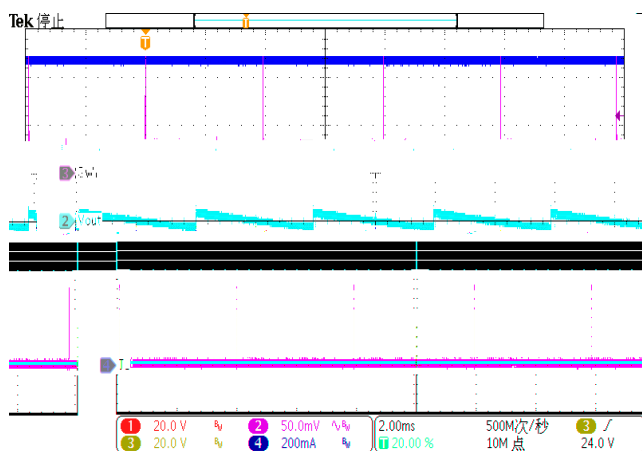
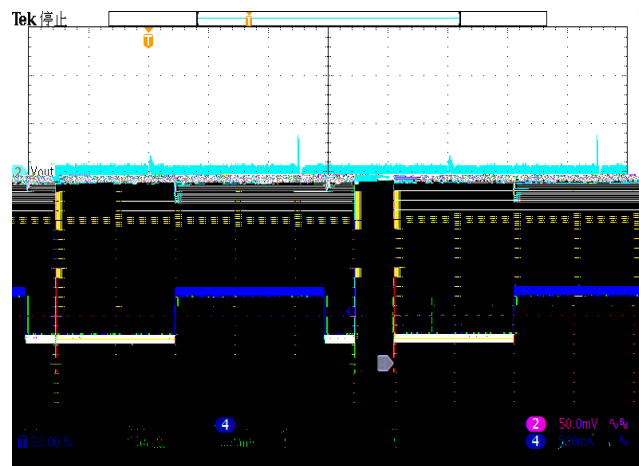
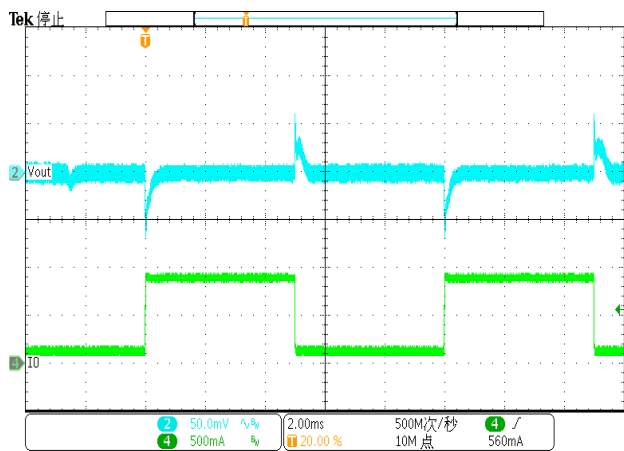
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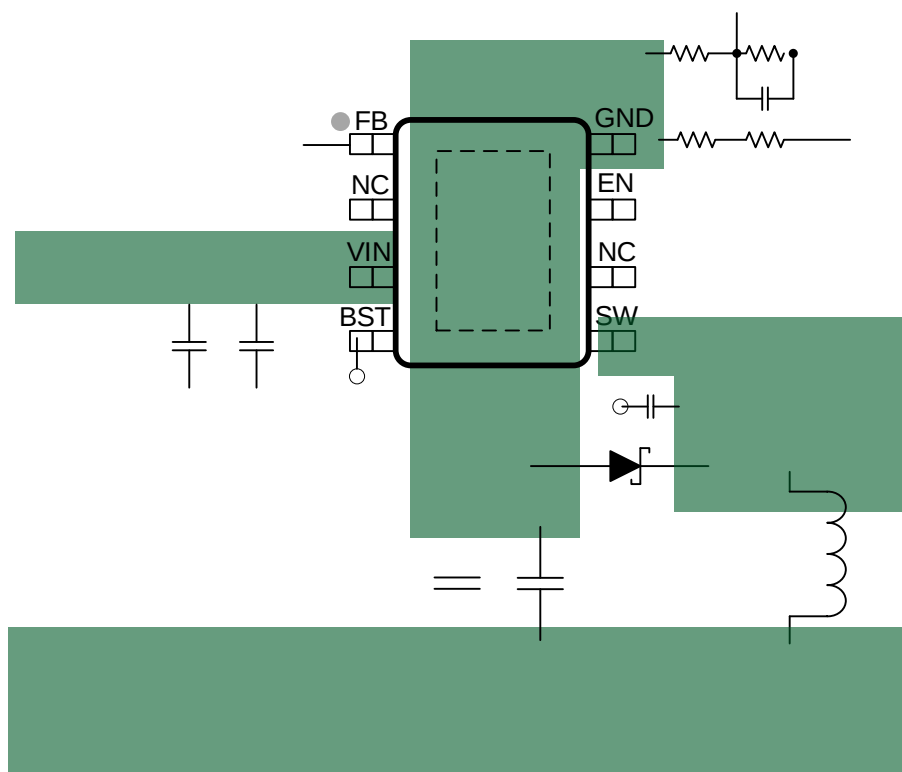
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3.

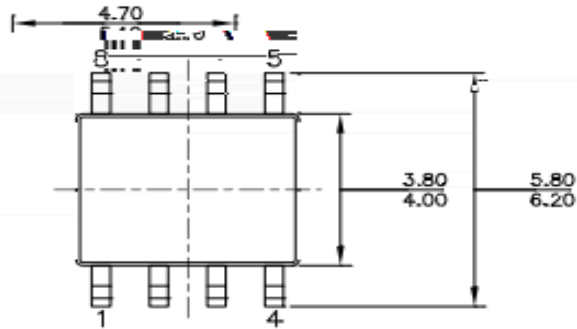
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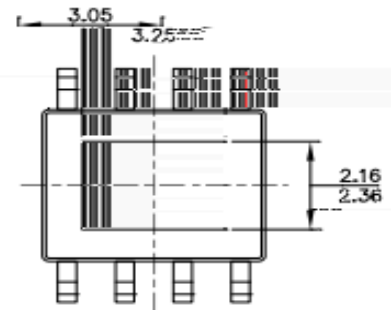
6.



PACKAGE OUTLINE DRAWING FOR 8L SOP-EP POD-0022 Revision 0.0

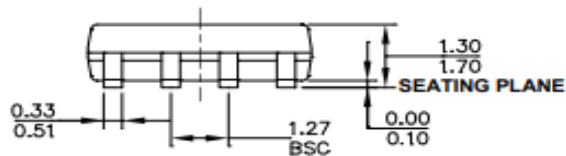


TOP VIEW



TOP VIEW

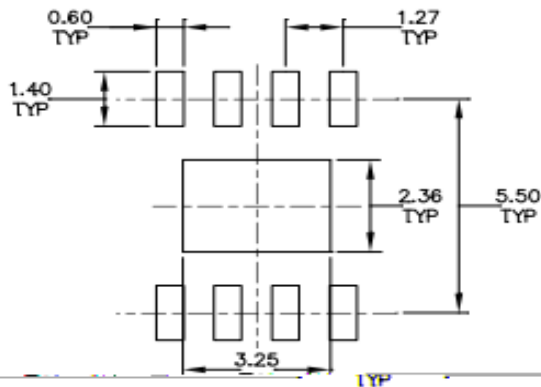
0.17
1.25



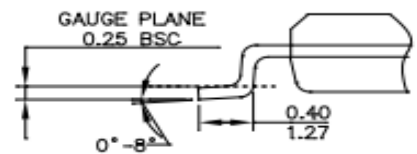
FRONT VIEW



SIDE VIEW



RECOMMENDED LAND PATTERN



DETAIL "A"

NOTE:

- 1) CONTROL DIMENSION IS IN INCHES. DIMENSION IN BRACKET IS IN MILLIMETERS.
- 2) PACKAGE LENGTH DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BUR
- 3) PACKAGE WIDTH DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
- 4) LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.004" INCHES
- 5) DRAWING REFERENCE TO JEDEC MS-012, VARIATION AA.
- 6) DRAWING IS NOT TO SCALE.

IRS.

S MAX.

